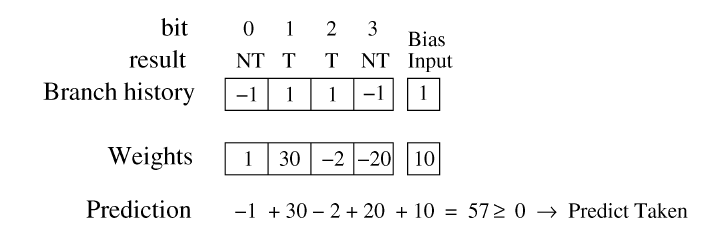
Proposal on designing a ML based branch predictor taking branch offset into account

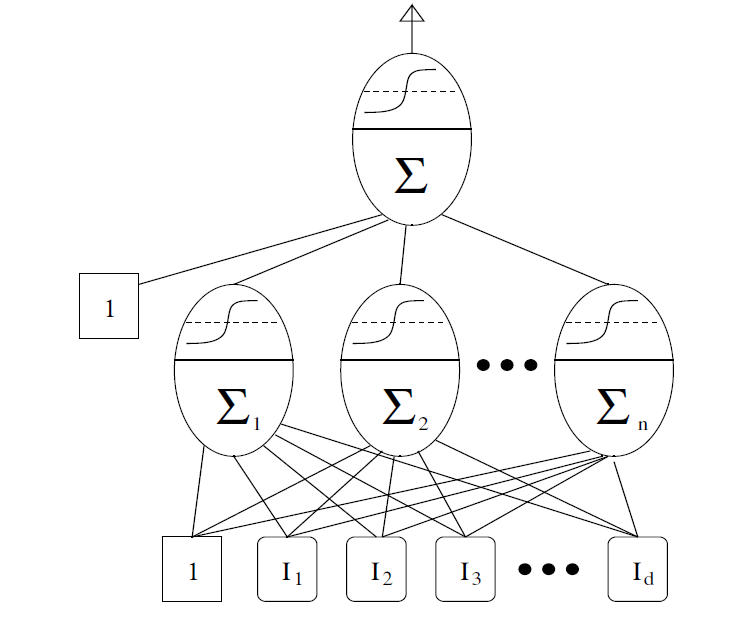
Prior work (ML based branch predictor I found on IEEE):

1997: Calder et al – pre-defined static decision tree

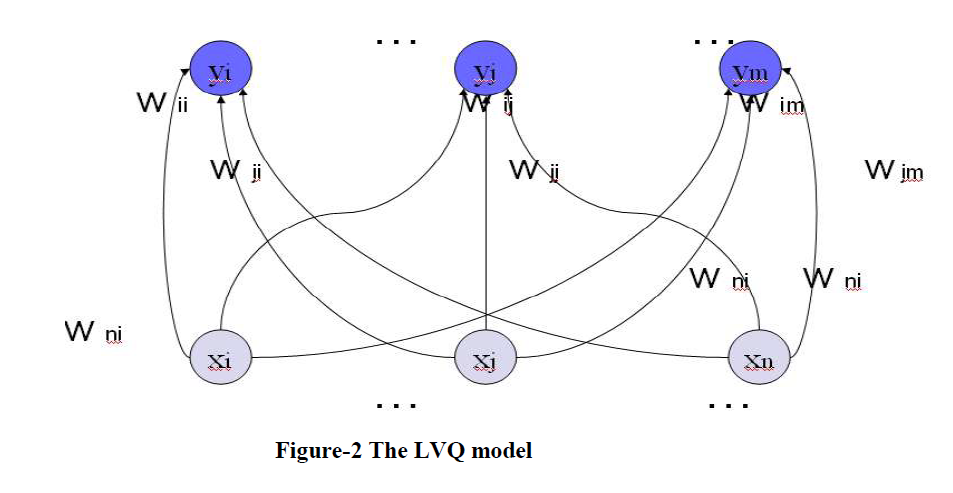
2001: Jimenez et al – weighted sum of past taken / not taken sequence. (least mean square), run-time training



2004: Smith et al - multi-layer of 1D data sequence, tested on different networks.

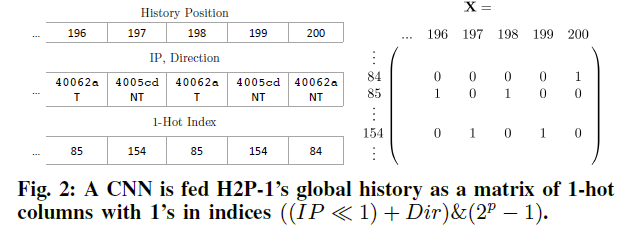


2005: Wang et al - vector – based “1.5D” training model.



2017 Mao et al - 6-layer 1D neural network

2019: Tarsa et al – convert past branch predictions into a one-hot matrix, and use 2-layer CNN, runtime.



2020: Mao et al – use DNN, deeper layer, but 1D (best performed have deepest network, 18 layers), pre-compiled.

Mao showed that the DNN based predictor performs excellent in media – related workload (uniform repetition, many fixed-sized or parameterized loops)

What I propose:

All prior on machine-learning based branch predictors are based on branch history, which is a sequence of 1s and 0s. This is totally understandable that branch predictor’s training should be computed in run time. And it’s also been addressed by Mao et al that such branch predictor performs excellent in media-related workloads. However, in my opinion, except from the past branch taken / not taken sequence, as well as the program-pointer contains information for predictor training, I take the assumption that the “step” parameter, which is the branch offset should also be taken into account during training. I find this assumption rather intuitive that in media-related workload, the loops in the program often have fixed iteration and the branch offset are static rather than dynamic, and such iteration pattern may occur multiple time in a data-stream type workload.

So, based on the assumption that the branch offset contains valuable information to training a branch predictor and propose the following training model:

The input is a sparse, one-hot 3D matrix with three axes: PC/taken, step size, and time;

The PC/taken axis design is referred from Tarsa et al (2019), that the value of PC/taken =

LSBs(PC of Branch Instruction) << 1 + taken/not taken(1/0)

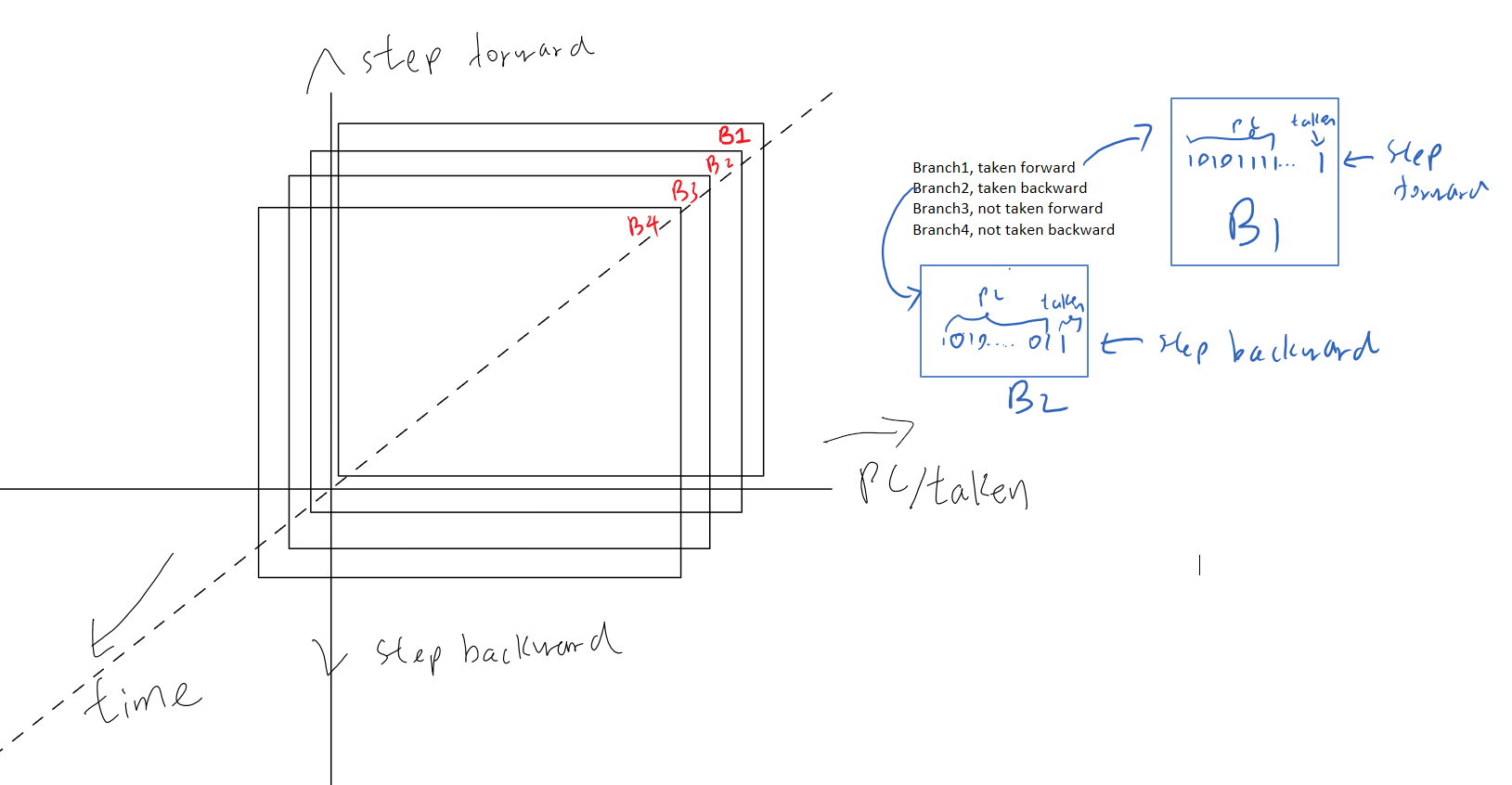
For example, Branch @ 0x0000\_1101 is taken, then the value of this axis is 0001\_1011

Branch @ 0x0000\_0101 is not taken, then that value of this axis is 0000\_1010

The step size axis is some\_activation\_function(branch imm) + offset,

The time axis is basically the branch pattern history

To visualize the 3D branch history information matrix, is shown:



As the data presented above, we can setup some pre-defined or come up with a 3D CNN model and evaluate whether including “step size” parameter into account would yield a nice result on certain workloads.